

Electronic Acknowledgement Receipt

EFS ID:	4127678
Application Number:	10517258
International Application Number:	
Confirmation Number:	1207
Title of Invention:	Semiconductor intergrated circuit device, data storage verification device, and data storage verification method
First Named Inventor/Applicant Name:	Katsuhiro Nakai
Customer Number:	00513
Filer:	Mark D. Pratt/pam veazey
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		AttachA_P0371701_AMEND_R EMARK.pdf	158091 72f46ae6a52a70e0d15ce246baa296262d8f d333	yes	11

	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment/Req. Reconsideration-After Non-Final Reject		1	1	
	Specification		2	2	
	Claims		3	8	
	Applicant Arguments/Remarks Made in an Amendment		9	11	
Warnings:					
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2	Specification	AttachB_P0371701_SPEC1.pdf	1660822	no	61
			99a4289f814dae089c9f58a5fe7d21f154f989c4		
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3	Specification	AttachC_P0371701_SPEC2.pdf	153860	no	61
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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